

Clean Set of Amended Claims

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Q1
15. (Amended) A circuit for a high power amplifier, comprising:
- a divider to divide an input signal into a first signal and a second signal;
  - a vector modulator to receive the first signal and a control signal and output a vector modulated signal;
  - an amplifier to amplify the vector modulated signal;
  - a directional coupler to receive a signal from the amplifier and generate a reference signal;
  - a delay to delay the second signal by a prescribed time period; and
  - a fast phase-amplitude controller to compare amplitudes and phases of the reference signal and the delayed second signal to provide the control signal.

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**Please add new claims 20-22 as follows:**

20. The vector modulator of claim 1, wherein the first amplitude invariant phase shifter adjusts a distribution of signals outputted from a combiner in a polar coordinate system by adjusting phases of incoming signals.

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21. The vector modulator of claim 13, wherein the first, second, and third amplitude invariant phase shifters respectively shift within first, second, and third prescribed shifting ranges.

22. The vector modulator of claim 21, wherein the combiner calculates a vector sum, wherein the first amplitude invariant phase shifter delays the input signal by fixed intervals within the first prescribed shifting range, wherein the second and third amplitude invariant phase shifters delay the first and second channel signals by first and second phases within the second and third prescribed shifting ranges respectively, and wherein the second and third prescribed shifting ranges are variable.